REMARKS/ARGUMENTS

Claims 1-16 are pending in this application. The Examiner has allowed claims 6-8 and 15. Allowance of these claims is appreciated. The Examiner rejects claims 1-5, 9-14 and 16. In particular, the Examiner rejects claims 1-2, 4-5, 10 and 12-13 under 35 U.S.C.§102(b) as being anticipated by Ishikawa et al., U.S. Patent No. 5,619,403. The Examiner also rejects claims 3, 14 and 16 under 35 U.S.C.§103 as being unpatentable over Ishikawa et al. The Examiner rejects claims 9 and 11 under 35 U.S.C.§103 as being unpatentable over Ishikawa et al.

By this amendment, Applicants have amended claims 1 and 12 to emphasize that the output circuit does not include an inductor. Ishikawa clearly requires an inductor in the output circuit. No such inductor is required in the output circuit according to the present invention. Accordingly, it is submitted that claims 1 and 12 are patentable over the Ishikawa reference. With respect to the Examiner's comments concerning to the lack of an output inductor, the amended claims now specifically exclude an inductor in the output circuit. Accordingly, it is submitted that the Examiner can not read the Ishikawa reference on the invention as now claimed since Ishikawa includes an inductance in his output circuit.

With respect to claim 11, Applicants disagree with the Examiner. The Examiner asserts that Ishikawa teaches or suggests that the isolation transformer comprises a printed circuit board transformer. The Examiner cites to column 16, line 17-25 and Fig. 22 of Ishikawa. The Examiner states that Ishikawa shows a diagram illustrating the use of a one chip IC design so that the apparatus can be provided in a small size and can be used for general purposes and the Examiner states that the transformer comprises a primary winding and a secondary winding deposited on opposite sides of a printed circuit board.

Applicant disagrees with the Examiner. Ishikawa merely states that in the circuit of Fig. 22 components enclosed with the broken lines, such as the PWM control circuits 1 and 4, the synchronism detection circuit 3, the pulse width limitation circuit 7, power supply voltage detection circuit 6 and the like can be provided on a one chip IC. However, the Examiner should note that the broken lines specifically exclude the transformer T1. Ishikawa does not state or suggest that the transformer can be provided on a printed circuit board. He merely states that the components enclosed by the broken lines can be placed into an integrated circuit. This does not

teach or suggest that the transformer can be a planar isolation transformer comprising a printed circuit or a transformer comprising a primary winding and a secondary winding deposited on opposite sides of a printed circuit board. Accordingly, Applicants have placed dependent claim 11 in independent form and submit that it is patentable.

In view of the above, Applicants submit that all claims in this application are now in condition for allowance, prompt notification of which is requested.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on June 14, 2005

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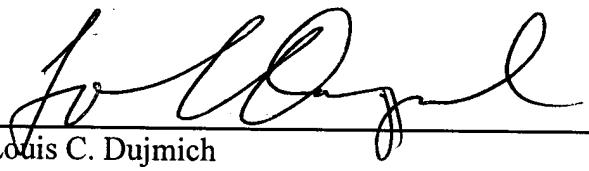
Name of applicant, assignee or Registered Representative

Signature

June 14, 2005
Date of Signature

Respectfully submitted,



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